

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	16108	((programmable adj3 circuit) OR PLD\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/08/09 14:28
L2	314	((programmable adj3 circuit) OR PLD\$1) and (verif\$4 near2 data)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/08/09 14:29
L4	158	((programmable adj3 circuit) OR PLD\$1) and (verif\$4 near2 data) and (wordline\$1 or row\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/08/09 14:31
L5	142	((programmable adj3 circuit) OR PLD\$1) and (verif\$4 near2 data) and (wordline\$1 or row\$1) and (bitline\$1 or column\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/08/09 14:31
L6	23	((programmable adj3 circuit) OR PLD\$1) and (verif\$4 near2 data) and (wordline\$1 or row\$1) and (bitline\$1 or column\$1) and (address near1 bit\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/08/09 14:32
L9	22	((programmable adj3 circuit) OR PLD\$1) and (verif\$4 near2 data) and (wordline\$1 or row\$1) and (bitline\$1 or column\$1) and (address near1 bit\$1) and select\$4	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/08/09 14:34
L10	22	((programmable adj3 circuit) OR PLD\$1) and (verif\$4 near2 data) and (wordline\$1 or row\$1) and (bitline\$1 or column\$1) and (address near1 bit\$1) and (select\$4 or multiplex\$3)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/08/09 14:35
L11	21	((programmable adj3 circuit) OR PLD\$1) and (verif\$4 near2 data) and (wordline\$1 or row\$1) and (bitline\$1 or column\$1) and (address near1 bit\$1) and (select\$4 or multiplex\$3) and shift\$3	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/08/09 14:36
L12	22	((programmable adj3 circuit) OR PLD\$1) and (verif\$4 near2 data) and (wordline\$1 or row\$1) and (bitline\$1 or column\$1) and (address near1 bit\$1) and (select\$4 or multiplex\$3) and register\$1	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/08/09 14:41
L13	13	((programmable adj3 circuit) OR PLD\$1) and (column adj2 shift adj2 register) and (row adj2 shift adj2 register)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/08/09 14:41

L14	6	((programmable adj3 circuit) OR PLD\$1) and (column adj2 shift adj2 register) and (row adj2 shift adj2 register) and verif\$6	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/08/09 14:42
L15	6	((programmable adj3 circuit) OR PLD\$1) and (column adj2 shift adj2 register) and (row adj2 shift adj2 register) and verif\$6 and (select\$5 or multiplex\$4)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/08/09 14:42